

DOCKET: BUR919990304US1

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Ference, et al.

Date: March 18, 2002

Serial No.: 09/732,240

Examiner: Chris C. Chu

Filing Date: 12/07/2000

Art Unit: 2815

Title: Multi-Chip Stack and method of Fabrication  
Utilizing Self-Aligning Electrical Contact Array#4  
Election  
FJONES  
3-19-02RESPONSE TO RESTRICTION REQUIREMENTAssistant Commissioner of Patents  
Washington, DC 20231

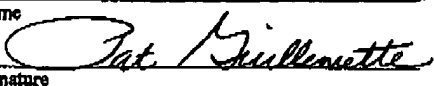
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MAR 18 2002

Dear Sir:

TECHNOLOGY CENTER 2800

Responsive to the Office Action mailed February 20, 2002, and the restriction requirement made therein, Applicants provisionally elect, with traverse, the subject matter of Group I, claims 1-14, drawn to a structure, for continued examination herein.

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited	
_____ with the United States Postal Service on the date indicated below as first class mail in an envelope addressed to the Assistant Commissioner of Patents and Trademarks, Washington, DC 20231.	
<input checked="" type="checkbox"/>	by facsimile, to the attention of: Examiner: Chris C. Chu Fax No.: 703-872-9318
Date:	3-18-02
Name	PAT GUILLEMETTE
Signature	

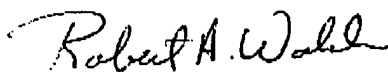
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Applicants traverse the restriction requirement on the grounds that 35 USC § 121 authorizes restriction only when the claimed invention is "independent and distinct" (emphasis added). A search of the subject matter of Group I, claims 1-14, drawn to a structure, would necessarily require a search of the subject matter of Group II, claims 15-29, drawn to a method of joining the structure. Thus the method delineated in Group II is not directed to an independent and distinct matter.

Accordingly, Applicants respectfully request that the Examiner reconsider and withdraw the requirement for restriction.

Respectfully submitted,



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